boland

Procedure - LVB, PVB, BVB FPGA upgrade

2015-03-20-BLM, 2016-06-04-mb, 7/13/17-mb.

Materials & Software

- 1. Requires BSC-100 vers. 3.0.1 or later (see BSC-100 manual for installation and full operation details)
- Reference: PC 2nd NIC setup procedure: NICquickprocedure_BSC_FPGA.pdf (for when PC is on office network subnet different from Monitor on second NIC card)

Assembly & Procedure

- 1. Connect monitor and PC to network router of same subnet (for example, 192.168.0.X).
- 2. Start BSC-100 and select 'FPGA ISP' under Tool Menu:

🗯 FPGA			X		
Monitor List on network					
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Build time File length Devices: E	natun : 2013-01-17 오車 7:39:0 : 2097340 brytes :PC 816	Program Verty			
Overall			Download		
] [0		

- 3. Click Refresh button (to find attached monitor[s]).
- 4. Select monitor in list (click on check-box).
- 5. Open FPGA.pof file (copied to local PC before disconnecting from Network [due to NIC Disable/Enable]).
- 6. Click-check the Program and Verify boxes
- 7. Click Download button
- 8. When status box says "IP Programming is successfully finished", connect another monitor, or exit program and reconnect to the network.

FPGA		🗲 FP/GA		
- Nonitor List on setwork		Monitor List on network		
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Overall]	Overal Coveral Download		
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